

IN THE CLAIMS

Please amend the claims as follows:

Claim 1 (Currently Amended): A semiconductor integrated circuit driven by an external power, comprising:

a change unit whose state changes with lapse of time without the external power,
wherein the change unit includes a plurality of change elements, each of the change elements whose state changes with lapse of time without the external power;

an output unit configured to output a signal in response to an instruction issued when the external power is supplied, the signal indicating a change of the state of the change unit;
and

an execution unit configured to execute a process in response to the signal.

Claim 2 (Original): The semiconductor integrated circuit according to claim 1,
wherein the output unit includes a plurality of output elements, each of the output elements outputting the signal in response to the instruction.

Claim 3 (Canceled).

Claim 4 (Currently Amended): The semiconductor integrated circuit according to claim [[3]] 1, wherein the output unit includes a plurality of output elements, each of the output elements outputting the signal in response to the instruction.

Claim 5 (Previously Presented): The semiconductor integrated circuit according to claim 1, further comprising a control unit configured to acquire time information based on the

signal when the external power is supplied, and control the execution unit to execute the process using the time information.

Claim 6 (Original): The semiconductor integrated circuit according to claim 5, wherein the output unit includes a plurality of output elements, each of the output elements outputting the signal in response to the instruction.

Claim 7 (Original): The semiconductor integrated circuit according to claim 5, wherein the change unit includes a plurality of change elements, each of the change elements whose state changes with lapse of time without the external power.

Claim 8 (Original): The semiconductor integrated circuit according to claim 7, wherein the output unit includes a plurality of output elements, each of the output elements outputting the signal in response to the instruction.

Claim 9 (Original): The semiconductor integrated circuit according to claim 1, further comprising:

an antenna connection unit connected to an antenna and configured to acquire a current induced in the antenna by electromagnetic induction; and

a power supply connected to the antenna connection unit, the power supply acquiring the current, rectifying and smoothing a acquired current, and supplying a rectified and smoothed current to an interior of the semiconductor integrated circuit.

Claim 10 (Original): The semiconductor integrated circuit according to claim 9,
wherein

the change unit includes a plurality of change elements, each of the change elements
whose state changes with lapse of time without the external power, and

the output unit includes a plurality of output elements, each of the output elements
outputting the signal in response to the instruction.

Claim 11 (Previously Presented): The semiconductor integrated circuit according to
claim 9, further comprising:

a control unit configured to control the execution unit to execute the process based on
the signal;

a demodulation unit configured to demodulate a command superposed on the acquired
current and output the command to the control unit, the demodulation unit being connected to
the antenna connection unit; and

a modulation unit configured to modulate a result of the process and output a
modulated result to the antenna.

Claim 12 (Original): The semiconductor integrated circuit according to claim 5,
further comprising:

an antenna connection unit connected to an antenna and configured to acquire a
current induced in the antenna by electromagnetic induction; and

a power supply connected to the antenna connection unit, the power supply acquiring
the current, rectifying and smoothing a acquired current, and supplying a rectified and
smoothed current to an interior of the semiconductor integrated circuit.

Claim 13 (Original): The semiconductor integrated circuit according to claim 12,
wherein

the change unit includes a plurality of change elements, each of the change elements
whose state changes with lapse of time without the external power, and

the output unit includes a plurality of output elements, each of the output elements
outputting the signal in response to the instruction issued when the external power is
supplied.

Claim 14 (Original): The semiconductor integrated circuit according to claim 12,
further comprising:

a demodulation unit configured to demodulate a command superposed on the acquired
current and output the command to the control unit, the demodulation unit being connected to
the antenna connection unit; and

a modulation unit configured to modulate a result of the process and output a
modulated result to the antenna.

Claim 15 (Original): A semiconductor integrated circuit module comprising:

a semiconductor integrated unit including:

a power supply which acquires a current from an antenna, and rectifies
and smoothes a acquired current, and supplying, as a power, a rectified and smoothed current
to an interior of the semiconductor integrated unit;

a change unit whose state changes with lapse of time without the
power;

an output unit configured to output a signal in response to an instruction issued when the power supply supplies the power, the signal indicating a change of the state of the change unit; and

an execution unit configured to execute a process in response to the signal;

a sealing material which seals the semiconductor integrated unit; and

an antenna terminal which connects the power supply to the antenna, the antenna terminal being exposed on an outer surface of the sealing material.

Claim 16 (Original): The semiconductor integrated circuit module according to claim 15, wherein the output unit includes a plurality of output elements, each of the output elements outputting the signal in response to the instruction when the power is supplied.

Claim 17 (Previously Presented): The semiconductor integrated circuit module according to claim 15, wherein the semiconductor integrated unit further includes:

a control unit configured to control the execution unit to execute the process based on the signal;

a demodulation unit configured to demodulate a command superposed on the acquired current and output the command to the control unit, the demodulation unit being connected to the antenna terminal; and

a modulation unit configured to modulate a result of the process and output a modulated result to the antenna.

Claim 18 (Previously Presented): The semiconductor integrated circuit module according to claim 15, further comprising a control unit configured to acquire time

information based on the signal, and control the execution unit to execute the process using the time information.

Claim 19 (Original): The semiconductor integrated circuit module according to claim 18, wherein the output unit includes a plurality of output elements, each of the output elements outputting the signal in response to the instruction when the power is supplied.

Claim 20 (Previously Presented): The semiconductor integrated circuit module according to claim 18, wherein the semiconductor integrated unit further includes:

a control unit configured to control the execution unit to execute the process based on the signal;

a demodulation unit configured to demodulate a command superposed on the acquired current and output the command to the control unit, the demodulation unit being connected to the antenna terminal; and

a modulation unit configured to modulate a result of the process and output a modulated result to the antenna.

Claim 21 (Currently Amended): An information apparatus comprising:

an antenna which acquires a current induced by electromagnetic induction;

a power supply connected to the antenna, the power supply acquiring a current, rectifying and smoothing the acquired current, and supplying, as a power, a rectified and smoothed current to an interior of the information apparatus;

a change unit whose state changes with lapse of time without the power from the power supply, wherein the change unit includes a plurality of change elements, each of the change elements whose state changes with lapse of time without power;

an output unit configured to output a signal in response to an instruction issued when the power supply supplies the power, the signal indicating a change of the state of the change unit; and

an execution unit configured to execute a process in response to the signal.

Claim 22 (Currently Amended): The information apparatus according to claim 21, wherein

~~the change unit includes a plurality of change elements, each of the change elements whose state changes with lapse of time without the power, and~~

the output unit includes a plurality of output elements, each of the output elements outputting the signal in response to the instruction when the power is supplied.

Claim 23 (Previously Presented): The information apparatus according to claim 21, further comprising:

a control unit configured to control the execution unit to execute the process based on the signal;

a demodulation unit configured to demodulate a command superposed on the acquired current and output the command to the control unit, the demodulation unit being connected to the antenna; and

a modulation unit configured to modulate a result of the process and output a modulated result to the antenna.

Claim 24 (Previously Presented): The information apparatus according to claim 21, further comprising a control unit configured to acquire time information based on the signal, and control the execution unit to execute the process using the time information.

Claim 25 (Original): The information apparatus according to claim 24, wherein
the change unit includes a plurality of change elements, each of the change elements
whose state changes with lapse of time without the power, and
the output unit includes a plurality of output elements, each of the output elements
outputting the signal in response to the instruction issued when the power is supplied.

Claim 26 (Previously Presented): The information apparatus according to claim 24,
further comprising:

a control unit configured to control the execution unit to execute the process based on
the signal;

a demodulation unit configured to demodulate a command superposed on the acquired
current and output the command to the control unit, the demodulation unit being connected to
the antenna; and

a modulation unit configured to modulate a result of the process and output a
modulated result to the antenna.